

ABSTRACT OF THE DISCLOSURE

Disclosed is a plasma display panel including a first substrate and a second substrate provided with a predetermined gap therebetween, and disposed substantially parallel to each other; a plurality of address electrodes formed on the first substrate; a first dielectric layer formed on a front surface of the first substrate, covering the address electrodes; a plurality of barrier ribs mounted on the first dielectric layer with a predetermined height to provide a discharge space; a phosphor layer formed within the discharge space; a plurality of discharge sustain electrodes provided on a front surface of the second substrate facing the first substrate, and disposed generally perpendicular to the address electrodes; a second dielectric layer formed on the front surface of the second substrate, covering the discharge sustain electrodes; and a passivation layer coated on the second dielectric layer, comprising MgO and dopant elements Si and Fe.

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